

# 2 Mbit Serial Spi Bus Eeprom

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## **Standard Serial EEPROM STMicroelectronics**

February 15th, 2019 - 64 Kbit serial SPI bus EEPROM with high speed clock 105°C operation

## **Serial Peripheral Interface Wikipedia**

February 16th, 2019 - The Serial Peripheral Interface SPI is a synchronous serial communication interface specification used for short distance communication primarily in embedded systems

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## **I<sup>2</sup>C Wikipedia**

February 16th, 2019 - I<sup>2</sup>C Inter Integrated Circuit pronounced I squared C is a synchronous multi master multi slave packet switched single ended serial computer bus invented in

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February 18th, 2019 - USB RS232 FTDI designs and supplies USB semiconductor devices with Legacy support including royalty free drivers Application areas include USB RS232 USB Serial

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SPI Full support

**UM10204 I2C bus specification and user manual**

February 17th, 2019 - Document information UM10204 I2C bus specification  
and user manual Rev 6 " 4 April 2014 User manual Info Content Keywords  
I2C I2C bus Standard mode Fast mode

**I<sup>2</sup>C Wikipedia la enciclopedia libre**

February 18th, 2019 - Historia El bus I<sup>2</sup>C fue introducido en 1982 por  
Philips para la comunicaci3n interna entre circuitos integrados como por  
ejemplo juegos de CD y televisiones

**I<sup>2</sup>C " Wikipedia**

February 18th, 2019 - I<sup>2</sup>C f1r englisch Inter Integrated Circuit im  
Deutschen gesprochen als I Quadrat C oder englisch I Squared C E^aE^a  
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